## AMENDMENTS TO THE SPECIFICATION

Please replace the second paragraph on Page 1 with the following amended paragraphs:

A level shifter is used in a system LSI including two or more power sources[[;]] The level shifter receives and out puts a plurality of signals, and operates with a different configuration.

For for example, as shown in FIG. 1, there has been known a level shifter proposed in Japanese application Laid-Open No. 63-152220 and so on. While a shifter is placed between blocks of different power supply voltages, Recently, in order to reduce the system LSI leakage current, it has been required that the level shifter should deal with the situation where one of the blocks goes down and power supply to the block is turned off off off of power supply to unused blocks.